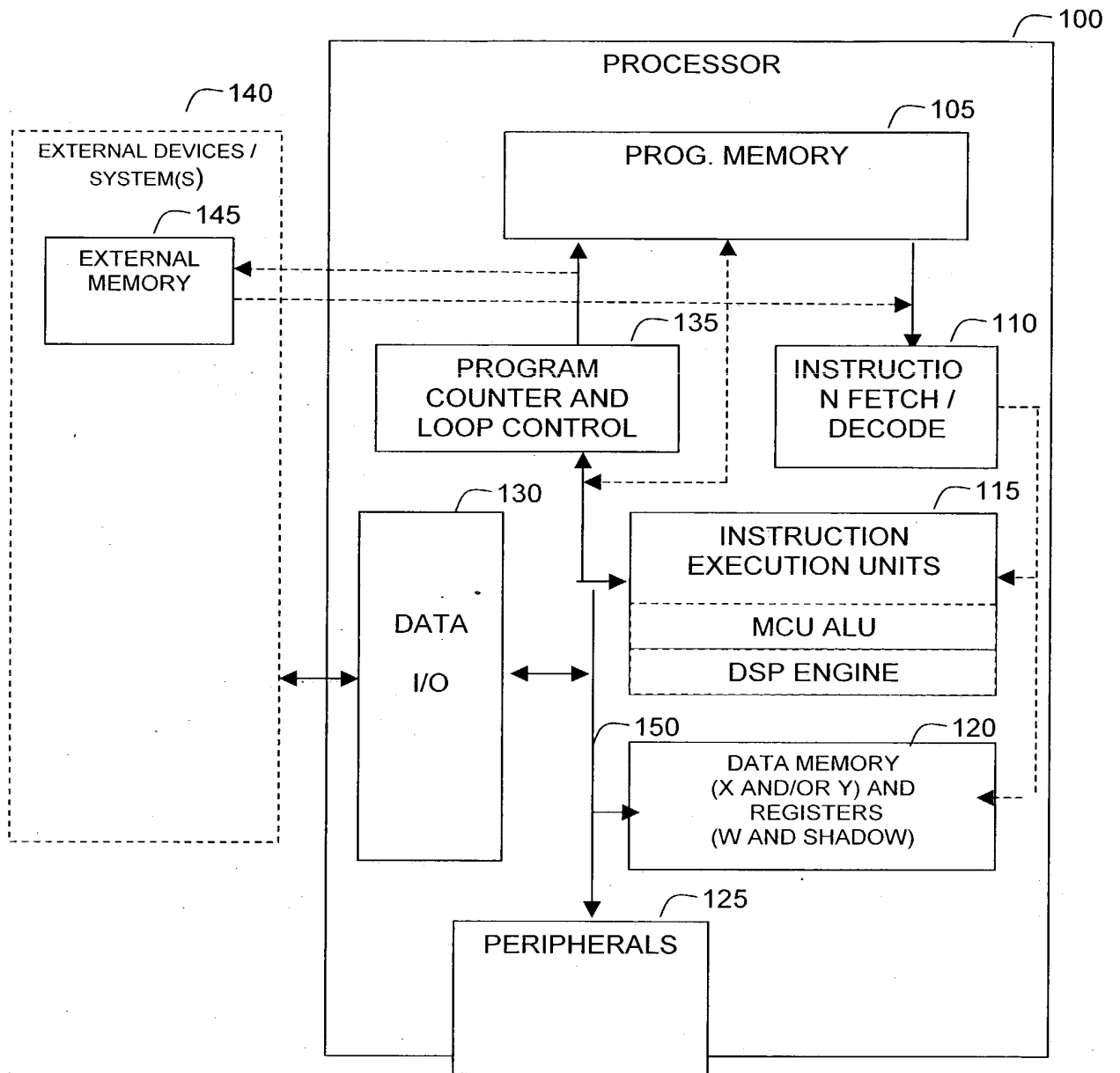
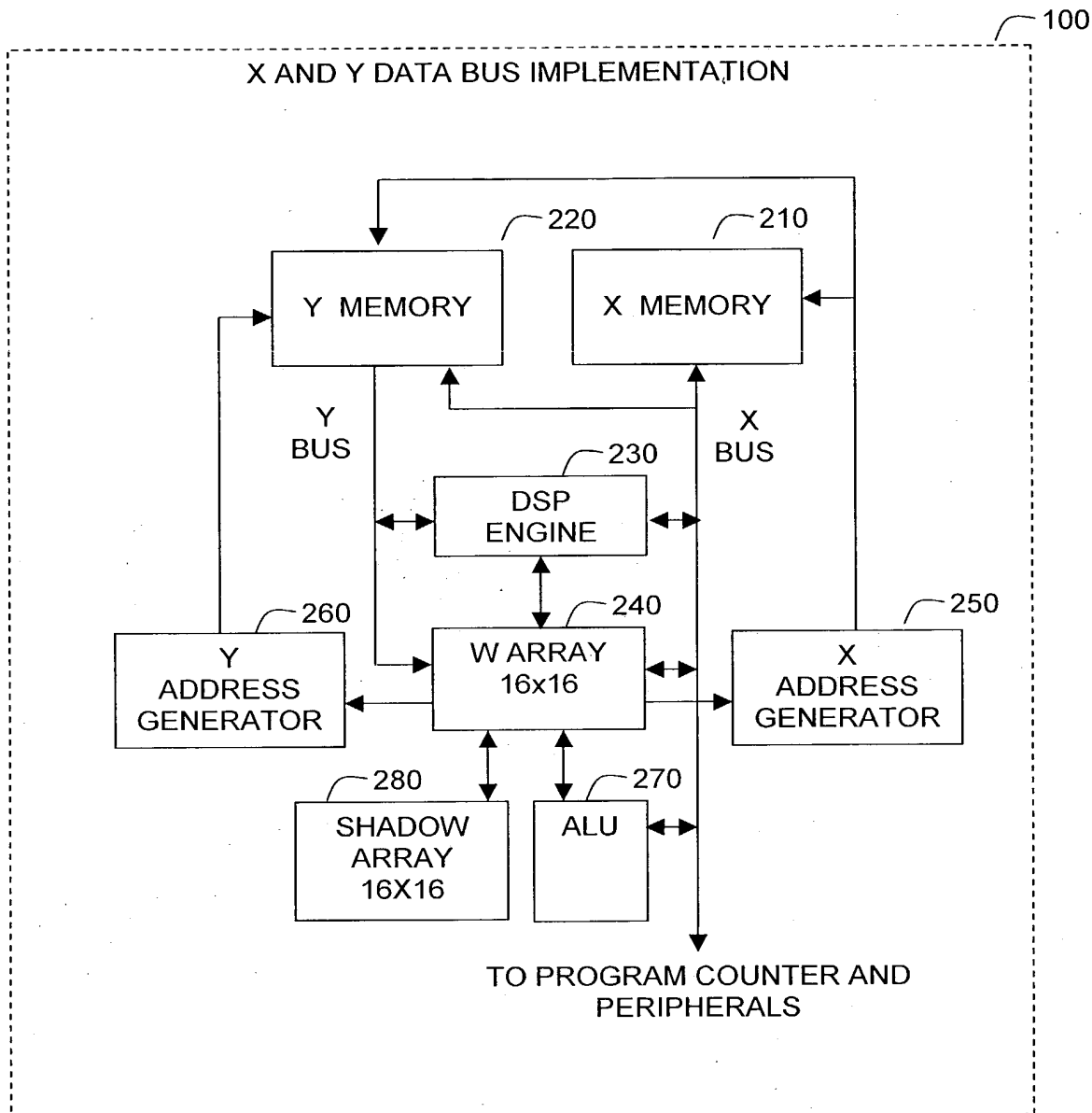


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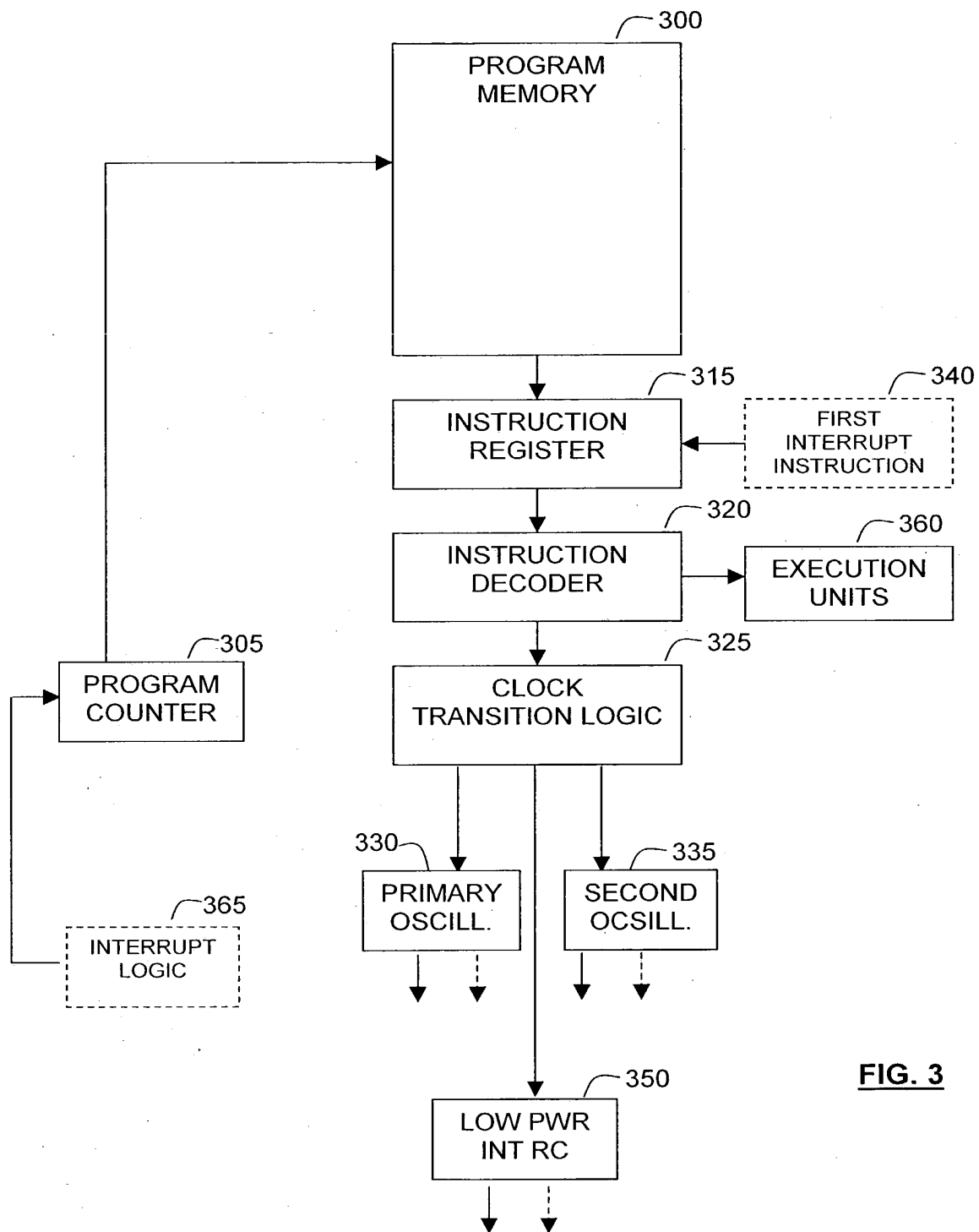


**FIG. 1**

TOP SECRET

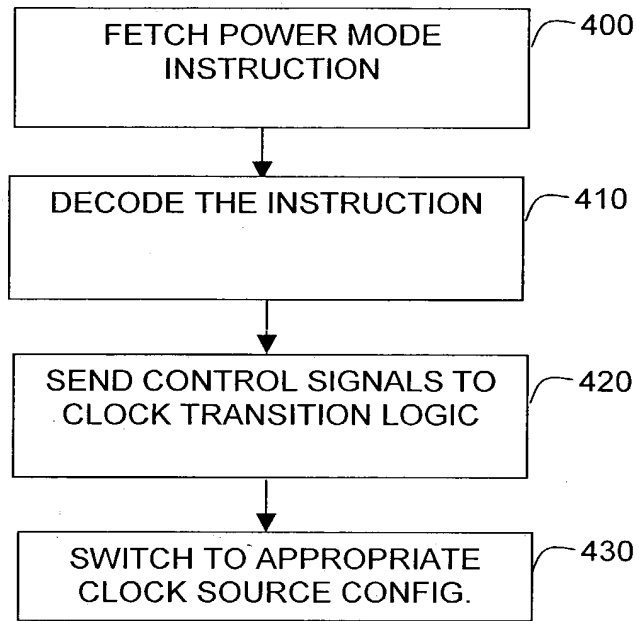


**FIG. 2**



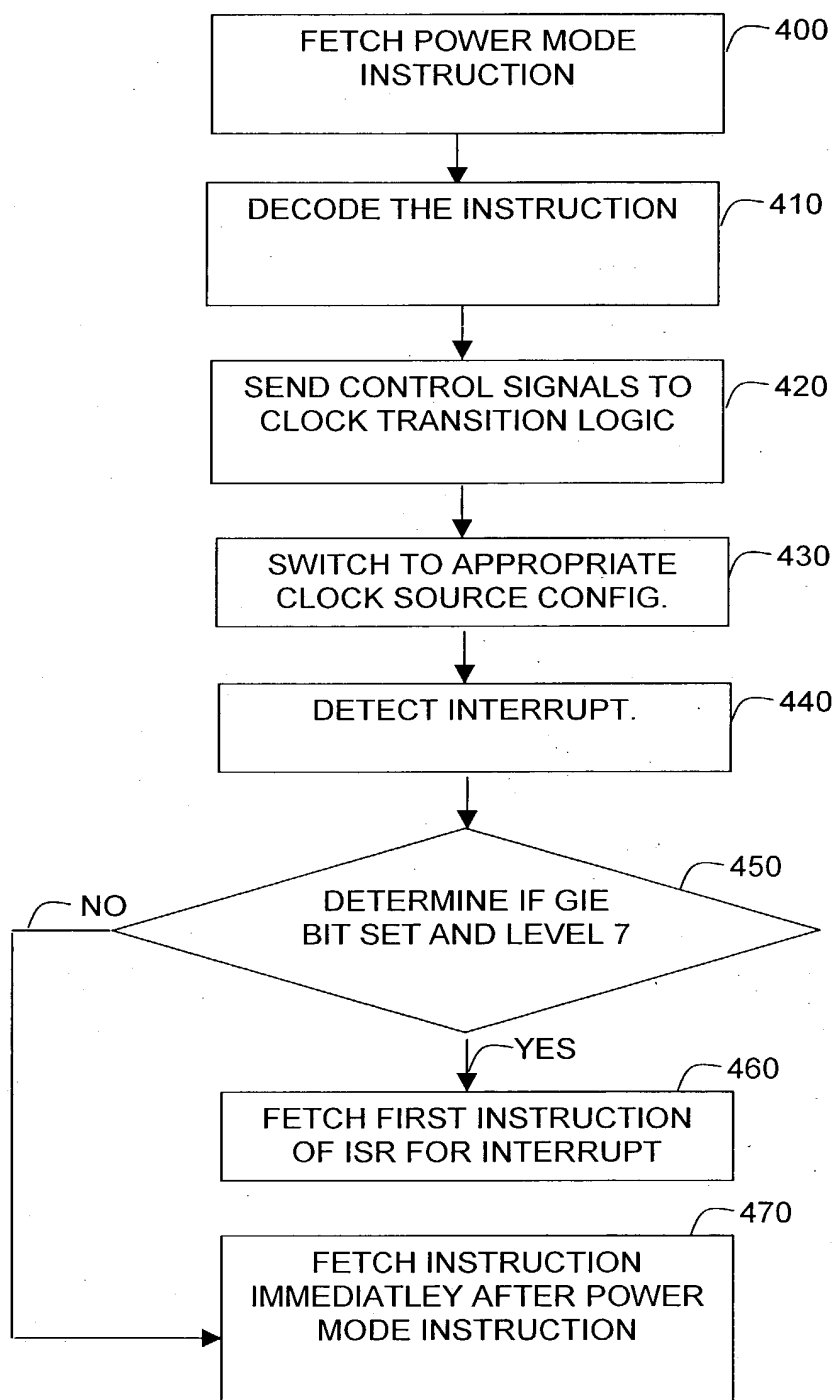
**FIG. 3**

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**FIG. 4A**

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101050" 22/20/850



**FIG. 4B**

# DEVICE OPERATING POWER MODES

MODE	LIT.	CPU CLOCK SOURCE	PERIP. CLOCK SOURCE
RUN	001	DERIVED FOR PRI. OSC.	DERIVED FOR PRI. OSC.
RC-RUN	010	LOW PWR. INTERNAL RC	LOW PWR. INTERNAL RC
SEC-RUN	011	DERIVED FOR SEC. OSC.	DERIVED FOR SEC. OSC.
SLEEP	100	DISABLED	DISABLED
RC-IDLE	110	DISABLED	LOW PWR. INTERNAL RC
IDLE	111	DISABLED	DERIVED FOR PRI. OSC.

**FIG. 5**

00000722.000001